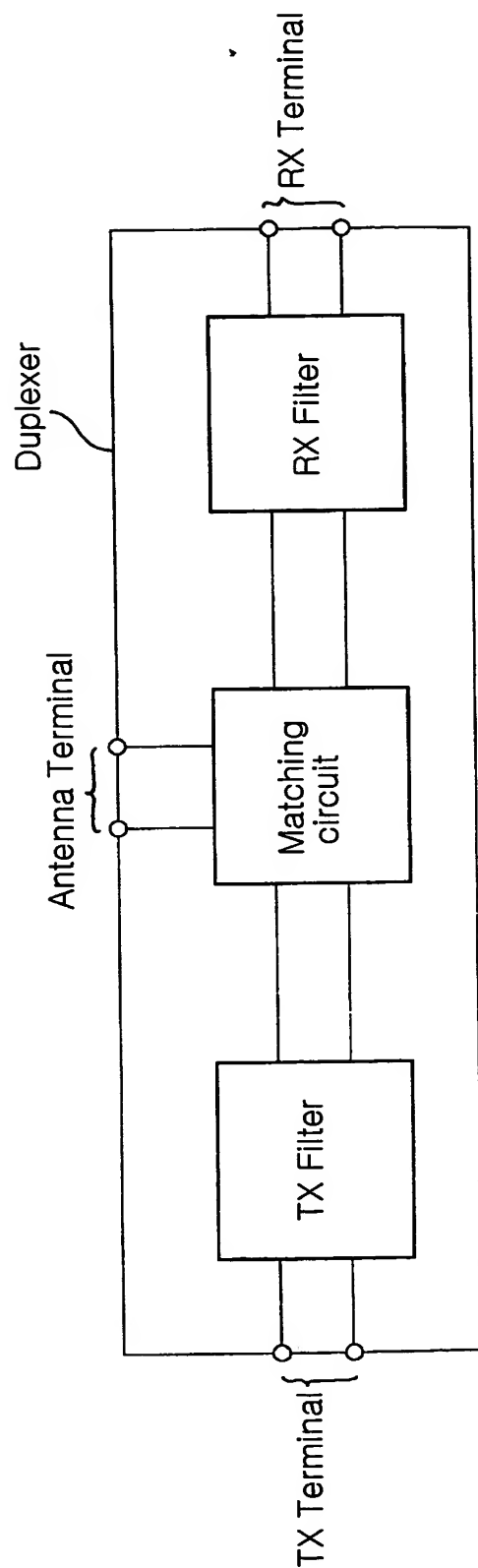


MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

Inventor: LEE, Byoung Hwa et al.

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PRIOR ART

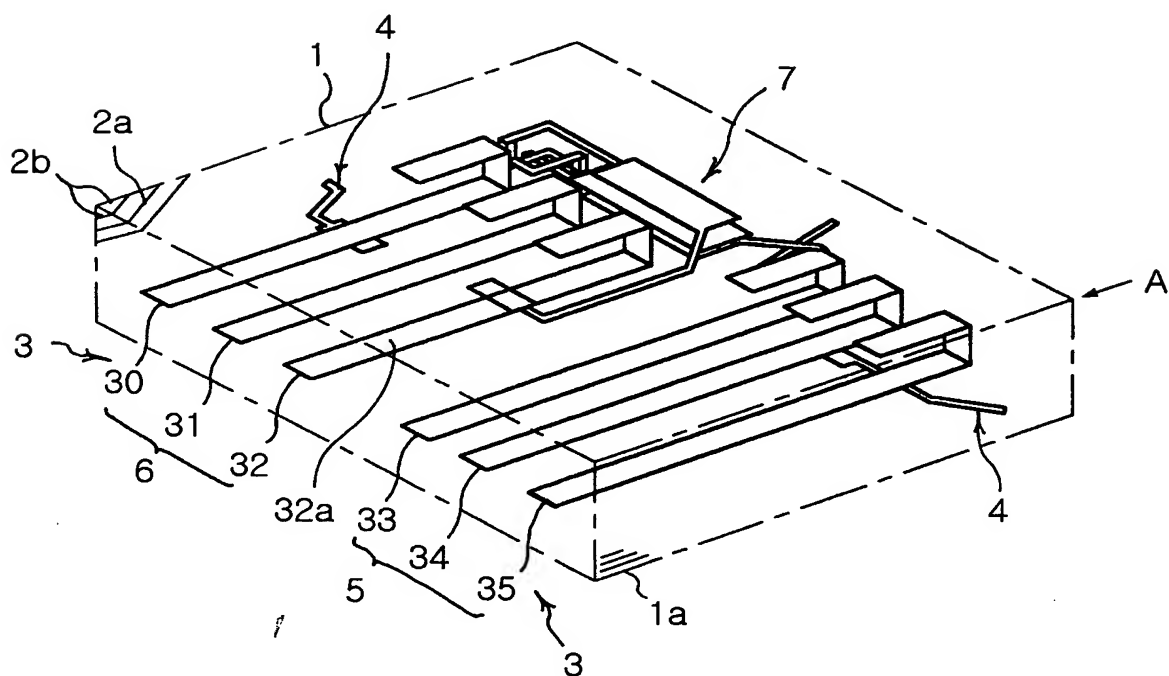
FIG. 1

MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

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PRIOR ART

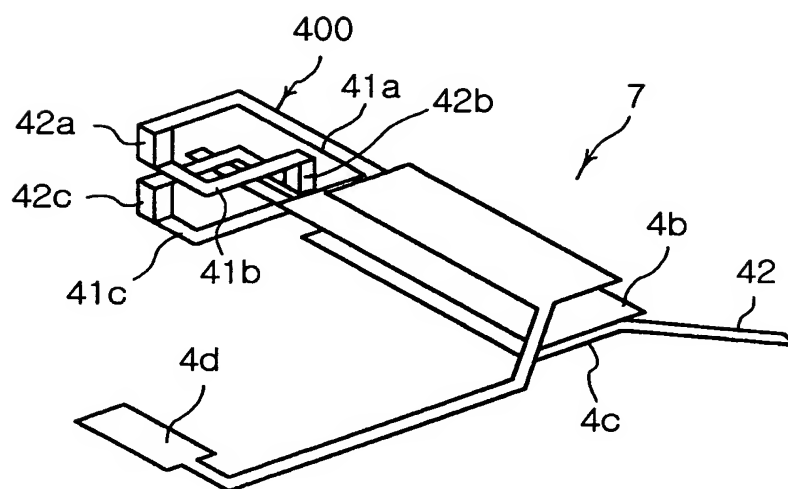
FIG. 2

MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

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PRIOR ART

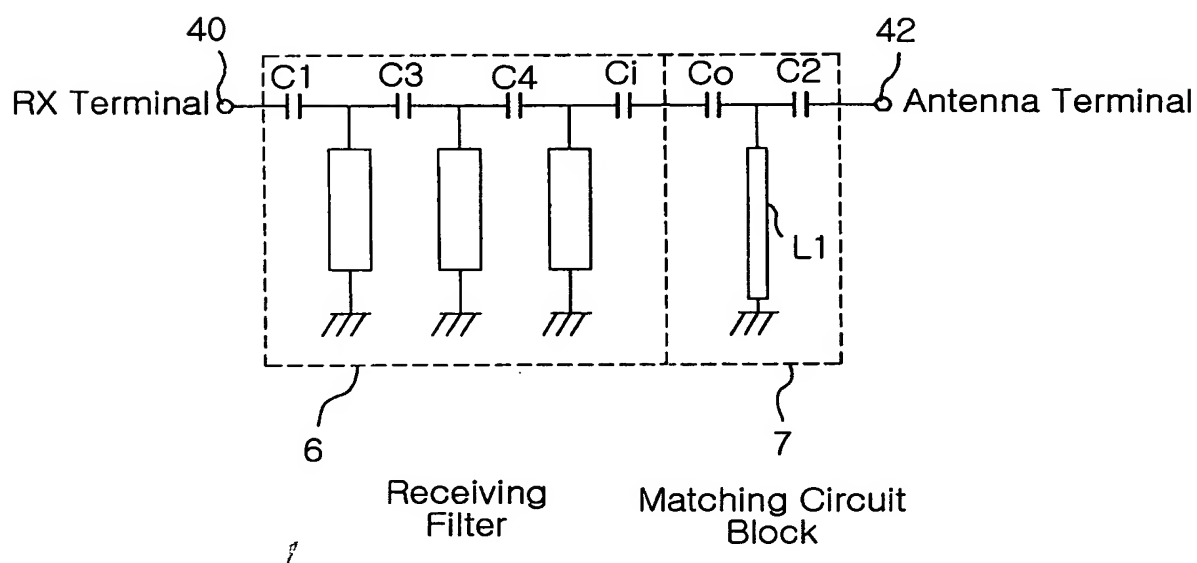
FIG. 3

MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

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FIG. 4

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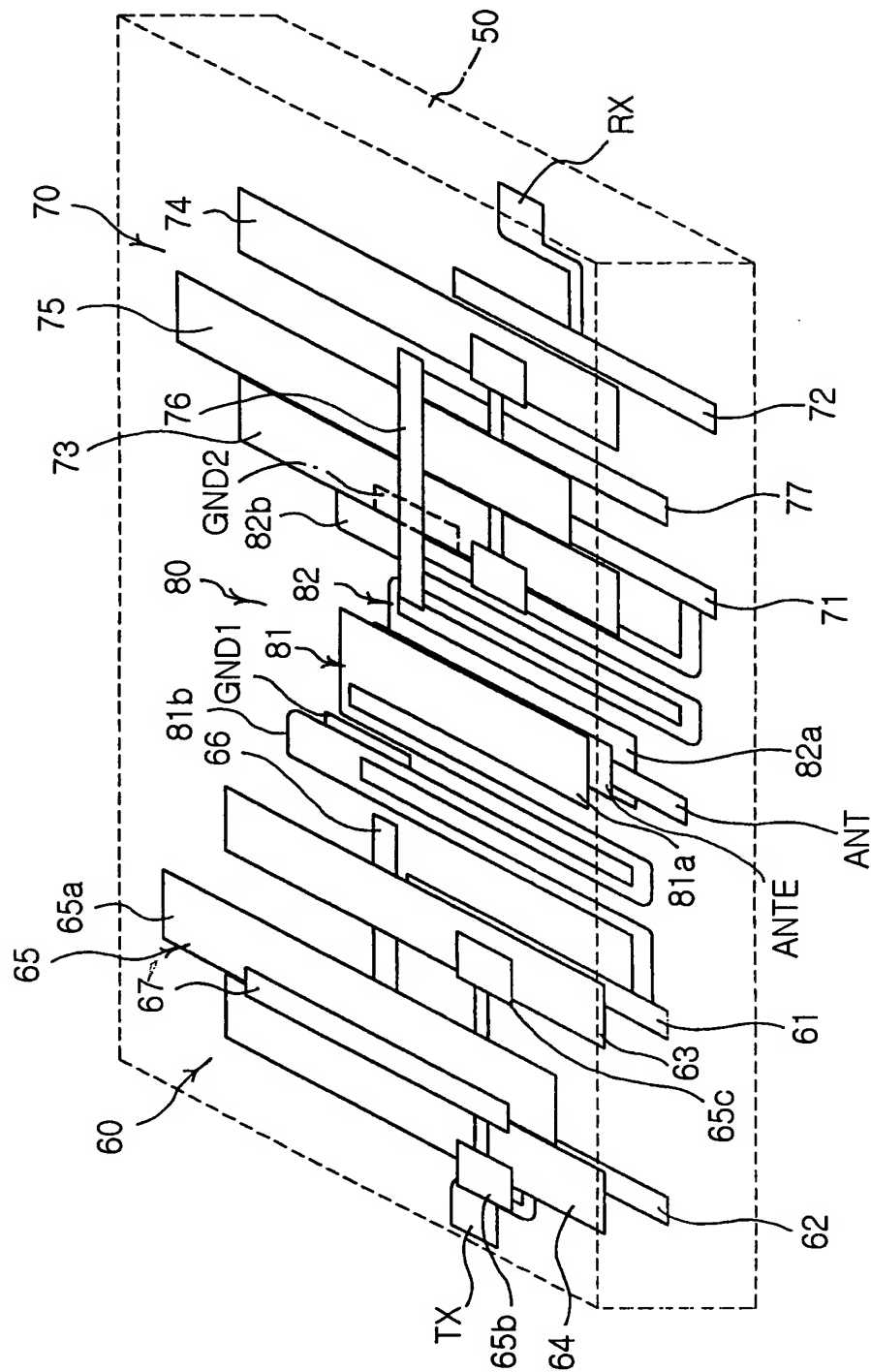


FIG. 5

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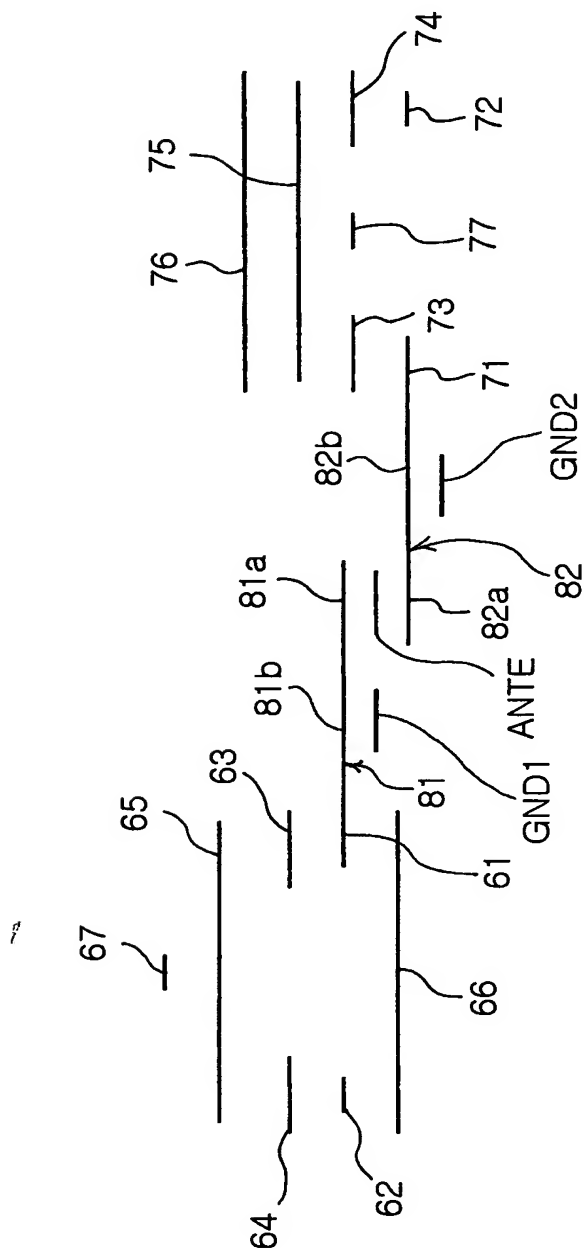


FIG. 6

MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

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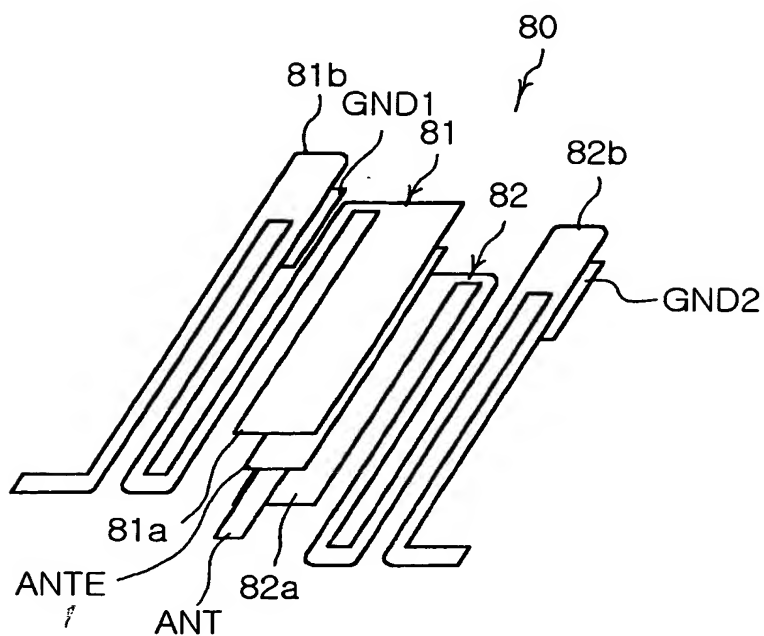


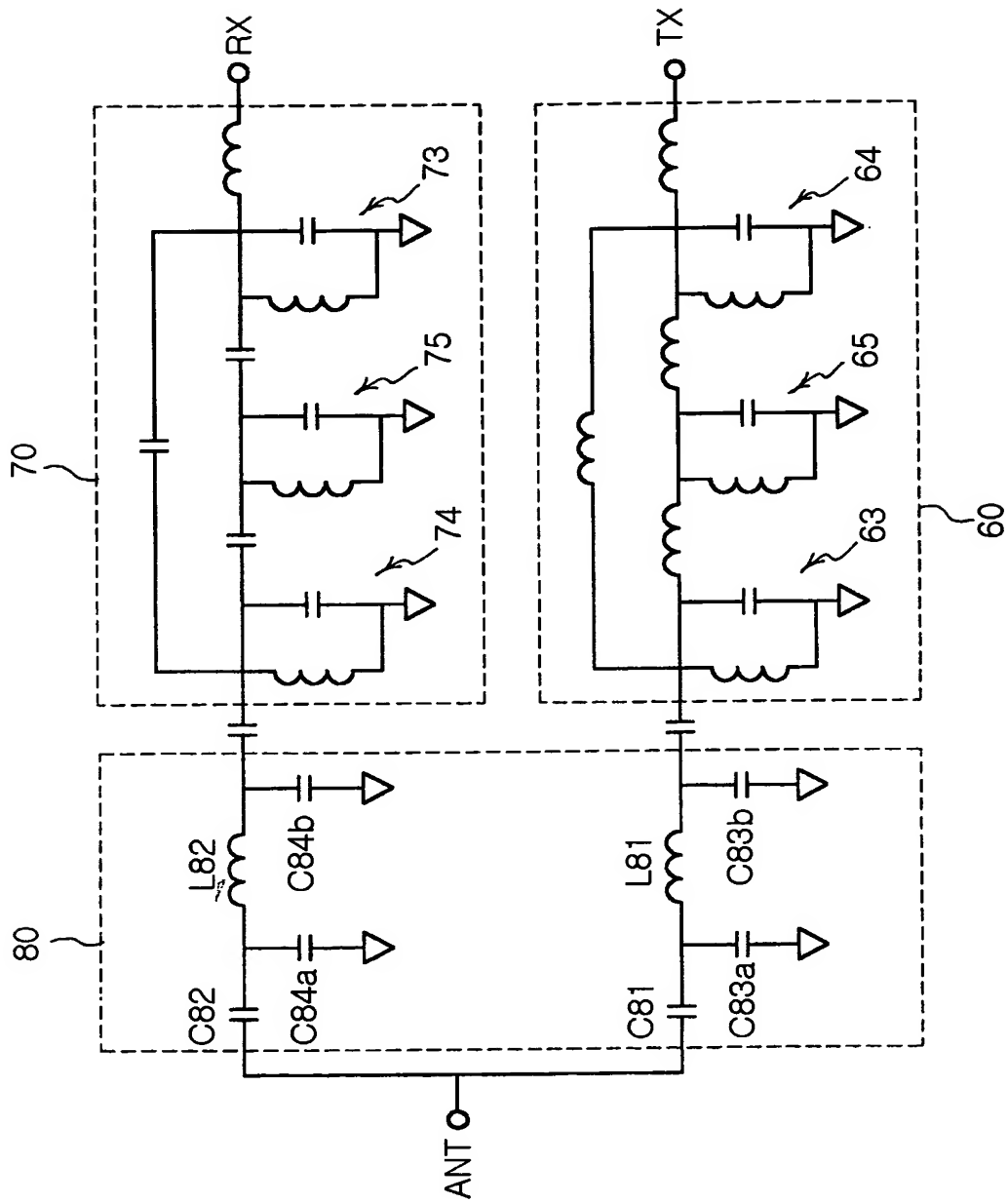
FIG. 7

MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

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MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

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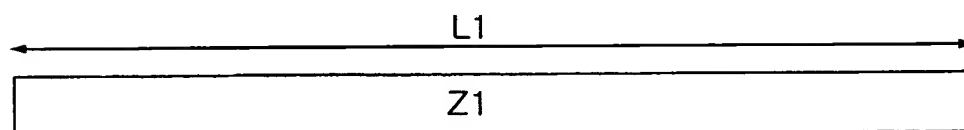


FIG. 9a

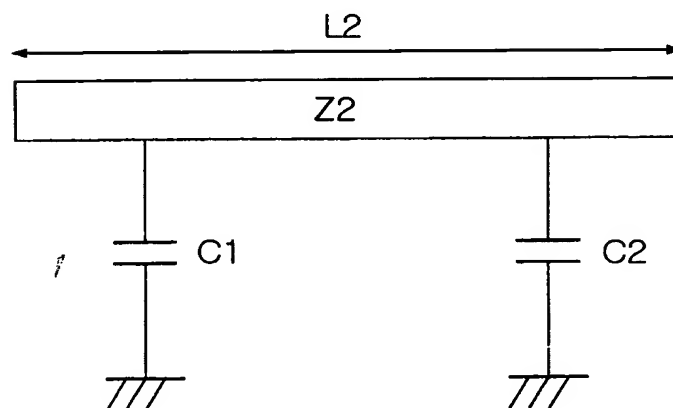


FIG. 9b

MATCHING CIRCUIT AND LAMINATED DUPLEXER WITH THE MATCHING CIRCUIT

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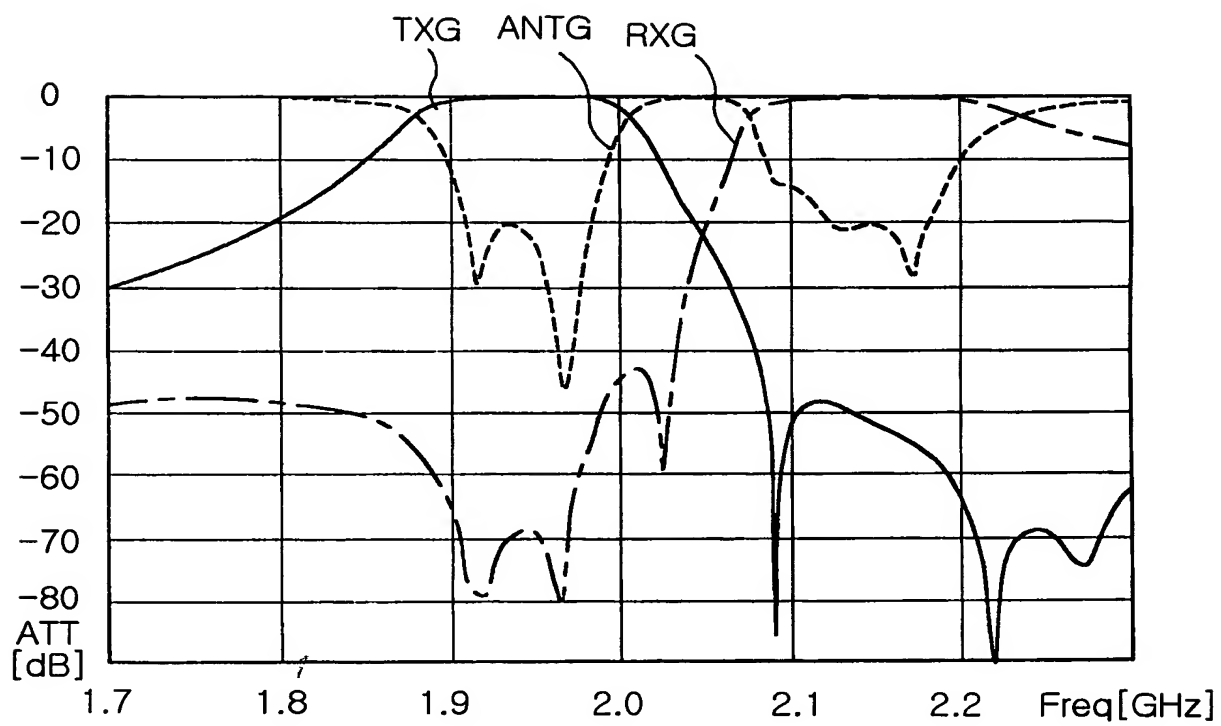


FIG. 10